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DESIGN & IMPLEMENTATION OF DIGITAL IMAGE PROCESSING USING FPGA



LAP Lambert Academic Publishing Nov 2011, 2011. Taschenbuch. Book Condition: Neu. 221x149x13 mm. Neuware - The criticality in handling the graphics data in an FPGA device has been addressed in this work. We have successfully shown how image data can be successfully converted to raw binary data and can be transferred as a file to the FPGA memory using the RS-232 link. Our design is very useful for those FPGA devices (low cost) which don't have a dedicated input graphics...

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